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Electrophysical Properties of Two Structured Polycrystal Silicon

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ABSTRACT: The article first describes the results obtained in the study of the electrophysical and charge transfer processes of two structural polycrystalline silicon obtained by bonding silicon particles with sunlight. The results of the study show that the charge transfer processes in such structures are found to be different from each other. In particular, at T~300-800 K, a decrease in the surface area of both structures μ was observed, and the temperature dependence of ρ and n differed from each other. For example, (a) in the surface area T~300-350 K and T~600-710 K ρ increases and n decreases, at T~350-550 K ρ decreases and *n* increases. Conversely, the surface area of the sample (b) is characterized by an increase in ρ and a decrease in n at T \leq 575 K, a decrease in ρ in the later stages of temperature increase, and an increase in *n*.

KEYWORDS: polycrystalline silicon, intergranular boundary, temperature, charge transfer processes, recombination centers.

INTRODUCTION

It is known that the physical properties of polycrystalline silicon (PC) based semiconductor devices or solar cells under certain conditions are explained by its microstructure and intergranular boundary (DCh) areas (e.g. [1-10], see also references given there). Studies have shown the potential for the production of relatively inexpensive and radiation-resistant solar cells or semiconductor devices as well as thermoelectric materials by controlling PC dimensional defects and introductory conditions in the DC field and charge transfer processes [1-7]. For example, it has been found that the introduction of additional alkali metal atoms in the DC field changes the conductivity [4, 5], improves the conductivity, adsorption or dissociation of the input atoms eliminates the recombination centers in the n-layer and improves the diode characteristics of the p-n structure [6, 7]. These processes depend on the granule size or DCh field microstructure and are important in obtaining p-n structures based on PK. For example, in the formation of the p-n structure, the diffusion of phosphorus across the DCh domains is stronger than the granulation in the formation of n layers by the introduction of phosphorus on the surface of r-type PK [4, 6, 7]. The process depends on the diffusion coefficient of the input atoms across the DCh areas relative to the granularity. Relatively speaking, the microstructure of DCh domains in PC size is formed in a similar way. However, in addition, for example, as noted above, it is of interest to study the charge transfer processes in two structural PCs that differ from the axial portion of the surface microstructure in which phosphorus is introduced during n-layer formation. This method can control the diffusion of phosphorus across relatively DCh areas. This paper discusses the results obtained in the study of the electrophysical properties of two structural PCs obtained on the basis of powder technology.

RESEARCH METHOD

It is known that powder technology is one of the promising methods in obtaining polycrystalline semiconductor devices and thermoelectric materials due to its simplicity and the fact that it does not require complex technologies [4, 5-9, 11]. The novelty of the study is that it is the first study of the electrophysical properties and charge transfer processes of two structural PCs obtained by heating and bonding silicon particles prepared on the basis of powder technology with sunlight.

The electrophysical properties of PC were improved using traditional four-probe and modernized experimental devices and methods of Vander-Pau methods. The temperature dependence of specific resistance (ρ), charge carrier mobility (μ)), and concentration (n) was studied in the process of temperature increase and decrease in the range ~300÷800 K.



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ELECTROPHYSICAL PROPERTIES OF SAMPLES

Figures 1 show the samples ρ and and Figure 2 shows the temperature dependence of n and μ . In order to facilitate the interpretation of the results of the study, we introduce the designations of the surface area (a) and the surface area (b) where the sunlight falls on the sample. It should be noted that T~300 K of the sample (a) surface area $\rho \sim 191.9 \text{ Om} \cdot \text{sm}, n \sim 3.11 \cdot 10^{18} \text{ sm}^3$, μ ~0,0245 sm²/Vs; (b), surface area ρ ~452 Om·sm, n~5,52·10¹⁷ sm³, μ ~0,025 sm²/Vs accepts values. The results of the study show that a decrease in μ (line 3) in both (a) and (b) surface area was detected. However, in the surface area (a) and (b) the temperature dependence of ρ and n differs from each other. For example, if (a) increases in the surface area at ~300 ÷ 350 K, n decreases (case a-b), if ρ decreases at T~350 ÷ 550 K, n increases (case b-c). Then, in the 50 K range, both parameters change relatively steadily (line 1). If ρ increases again at ~600 ÷ 710 K, it decreases to n 775 K (c-d condition). At T~710 ÷ 775 K, the sag change of ρ (d-e state) was detected. Conversely, if the surface area of the sample (b) increases at ρ T \leq 575 K (a-b 'state), n decreases (a-b-s' state), in the later stages of temperature increase (b-state) As ρ decreases, n (state c'-d ') increases (line 2). The results of the study can be interpreted as follows based on the microstructure model and zoning diagram of the sample shown in Figure 3. Figure 3 shows a model of the microstructure structure of the surface areas (a) and (b) of the sample and a zone diagram of the DC area (c). The structure of the sample can be conditionally determined by its granularity (1) and DC areas (2). The process of charge transfer in a polycrystalline semiconductor is mainly explained by the DCh field [1, 2]. The charge carriers move from area A to area B through the DCh fields (2) (Figure 3). In the WC field, strongly unbound and disconnected bonds form recombination centers of different Eip energy levels (Fig. 3c). They are localized in the DCs and create a barrier effect.



Figure 1. Temperature dependence of specific resistance.



Figure 2. Temperature dependence of the concentration and mobility of charge carriers.

The capture and re-release of charge carriers in localized traps results in a change in the potential barrier height (ϕ), which in turn ρ . According to the Setto model, the following expression can be written for ρ [1, 2]:

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$$\rho = \frac{k}{q\langle a \rangle A^*T} \exp(\frac{q\varphi}{kT}) \tag{1}$$

where, q - electron charge, k – Boltsman's constant, $\langle a \rangle$ - granularity measure, A^* - Richardson's efficiency constant, - temperature.

Depending on the amount of charge (Q_i) trapped in φ gi localized traps in the WC fields:

$$\varphi = \frac{Q_l^2}{8\varepsilon\varepsilon_0 q N_G} \tag{2}$$

where N_G is the concentration of electrically active alloyed inputs, ϵ and ϵ_o are the relative dielectric constant of the medium and the vacuum, respectively.



Figure 3. The microstructure model of the sample and the mechanism for explaining the charge transfer process.

Expression (2) shows that an increase in Q_i in the DC field (field 2) leads to an increase in φ , which in turn leads to an increase in ρ. For example, in [4, 5, 8-10], T~300÷350 K at E_{ip1}~0,15 eV and E_{ip2}~0,17 eV; T~383÷400 K at E_{ip3}~0,36 eV; T~400÷680 K at E_{ip3} ~0,3 eV it was found that the appearance of levels leads to an increase in PK ρ . It has also been shown that the electrophysical and photoelectric properties of PK and the p-n structures derived from it depend on the concentration of localized traps at the Eip energy level. The comments made are also relevant to our results. However, in our case, it was observed that the change in ρ differed according to the microstructure of the surface areas (a) and (b). In our opinion, this may be due to the nature of the manifestation of recombination centers in the WCs in the surface areas (a) and (b). First, (a) consider the surface area microstructure model and charge transfer processes. As mentioned above, (a) the surface area consists of a microstructure in which the silicon particles are solid and cohesive. In our opinion, in the initial stage of temperature increase, i.e. at T~300 \div 350 K, energy levels E~0.15 eV and E~0.17 eV appear. An increase in Q_i captured in them leads to a decrease in n and an increase in ρ (Figures 1 and 2, case a-b). As the temperature increases, the E_{ip} energy level increases relative to the concentration of localized traps *n* separated from the valence band. This leads to a decrease in ρ at T~350 ÷ 550 K (in area b-c). At T~550 \div 600 K, the concentration of n and localized traps separated from the valence band is equal, and in this case ρ changes steadily. In the later stages of the temperature increase, the concentration of localized traps of the Eip energy level in the DC fields again increases with respect to n separated from the valence band. T~600 \div 710 K leads to an increase in Q_i and ρ (case c-d). The appearance of new Eip energy-level recombination centers in accordance with the temperature may lead to a sag change of ρ (d-e state) at T~710 ÷ 775 K.

Now (b) consider the charge transfer processes in the surface area. As mentioned above, (b) the surface area (a) consists of a rougher surface than the surface area. Its microstructure (a) differs relative to the surface area. That is, the width of the DC field (a) is wider than the surface area (Fig. 2a). In this case, the charge transfer process can be carried out simultaneously through the DC fields and across the DC fields [3-5, 9]. Due to the relatively wider DC field, the area will have a higher number of strongly unbound and disconnected bonds that form localized traps at the E_{ip} energy level. Accordingly, there is also a high probability of the formation of localized traps of the E_{ip} energy level at different temperatures. The results of the study show that the increase in ρ at T \leq 575 K (a-b 'state) is consistent with the results obtained in the works [3-5, 9]. That is, at

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T~300÷600 K, recombination centers of E_{ip1} ~0,15 eV, E_{ip2} ~0,17 eV, E_{ip3} ~0,36, E_{ip3} ~0,3 eV appear in series. An increase in the amount of charge carriers Q_i in them leads to an increase in ρ at T~600÷710 K (a-b 'state). A decrease in n in the same temperature range (a'-b'-s' state) and a jump in certain temperatures may confirm the observations. That is, the jump change at T~325÷400 K corresponds to the formation of levels E_{ip1} ~0,15 eV, E_{ip2} ~0,17 eV and E_{ip3} ~0,36 eV. In the later stages of temperature rise, localized traps of the E_{ip} energy level in the DC field (2) begin to ionize. As a result, the charge carriers simultaneously move from area A to area B through the DC fields (2) and along the localized traps (Fig. 3b). The migration of charge carriers along localized traps leads to the formation of J_{ss} (Fig. 3s) [3, 4, 9]. This current occurs during the capture and rerelease of localized traps in the E_{ip} energy level above the E_f farm level as the charge carriers move from left to right. J_{ss} depends on the total conductivity (Y_{ss}) of the current traps, i.e., the total conductivity and the potential barrier height ($\delta \varphi$), which depends on the distribution of the traps across the granular surfaces and energy.

$$J_{ss} = Y_{ss} \delta \varphi \tag{3}$$

In this case, φ does not decrease, Y_{ss} increases. In studies [3, 4, 9], it has been found that the migration of charge carriers along localized traps leads to an increase in Y_{ss}. In our opinion, the above comments are valid for ρ (Fig. 1, b'-condition). An increase in the ionization process of localized traps leads to an increase in *n* (Fig. 2, line 2, c'-d 'state).

CONCLUSION

In summary, samples from which silicon particles are attached to sunlight have two different structures of polycrystalline silicon, and the microstructure of the surfaces on which sunlight falls and does not fall, as well as the charge transfer processes, are different. The method of obtaining PC by attaching silicon particles with sunlight can play an important role in obtaining two structural semiconductor materials for microelectronics and photovoltaics.

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